List of Bugs in BSIMSOI3.1 beta and Fixed in BSIMSOI3.1

Bug Reporters:
Meng-Hsueh Chiang (AMD) meng-hsueh.chiang@amd.com
John Faricelli (HP) john.faricelli@hp.com
Weidong Liu (Synopsys) Weidong.F.Liu@synopsys.com
Larry Wagner (IBM) WagnerL@us.ibm.com
Richard Williams (IBM) rqw@us.ibm.com
Josef Watts (IBM) jswatts@us.ibm.com
Pin Su (UCB) pinsu@eecs.berkeley.edu
Hui Wan (UCB) wanh@eecs.berkeley.edu
Yannick Mourier (Silvaco) yannick.mourier@silvaco.com

Description of Bug	Involved C-file	Bug Reporter	Fixed by
1. Reduce gmin between gate and drain by 6 order of magnitude	B3soild.c	M-H Chiang	M-H Chiang
2. Scale pParam->B3SOIAechvb and pParam->B3SOIAechvbEdge by NSEG	B3soitemp.c	J. Faricelli	J. Faricelli
3. Fix the Igb bug in the AC matrix stamping	B3soiacld.c	W. Liu	P. Su
4. Fix the source/drain swapping error of the source/drain overlap capacitance	B3soild.c	L. Wagner, R. Williams	L. Wagner, R. Williams
5. A_{bulkCV} in Q_{inv}	B3soild.c	L. Wagner, R. Williams, J. Watts	H. Wan
6. qgmid is added for the computation of the associated charge current	B3soild.c	Y. Mourier	H. Wan